PATENT ABSTRACTS OF JAPAN

(11)Publication number:

06-029684

(43) Date of publication of application: 04.02.1994

(51)Int.CI.

H05K 9/00

(21)Application number: 04-180165

(71)Applicant: FUJITSU LTD

(22)Date of filing:

08.07.1992

(72)Inventor: TAKASE MANABU

(0)

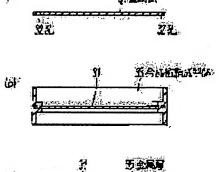
TAKAGI HISAMITSU NAKAMURA HIDEFUMI

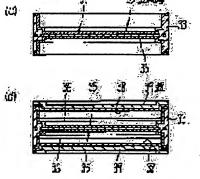
(54) SHIELD CASING

(57)Abstract:

PURPOSE: To make a shield casing accommodating high frequency circuits thin and light, and facilitate precise formation, by integrally molding synthetic resin in a unified body with the necessary part of a metal plate, and coating the surface of the synthetic resin molded object with a metal layer.

CONSTITUTION: After a metal plate 31 wherein many holes 32 of small diameter are formed at small intervals on the periphery is formed, a synthetic resin molded object 33 is formed, by outsert molding, in a frame type surrounding the periphery containing the holes 32 of the metal plate 31. After a metal layer 35 of suitable thickness is stuck and formed on the inner surface of the synthetic resin molded object 33 containing the metal plate 31 by electroless plating, evaporation, etc., printed boards 36 are fixed with screws or the like on the step parts of inner both sides of the synthetic resin molded object 33. Lids 39 on which metal layers 38 are stuck and formed in the same manner are fixed on apertures on both sides of the synthetic resin





1

molded object 33. Hence the printed board 36 is surrounded by the peripheral contact of the metal layer 35 of the synthetic resin molded object 33 and the metal layer 38 of the lid 39, and a shield cabinet for electrically shielding circuits from the outside is constituted.

LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration] [Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office